

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Sandeep K. GUPTA

Appl. No. *To Be Assigned*  
(Divisional of Appl. No. 09/911,498)

Filed: February 23, 2004

For: **Replica Network for Linearizing  
Switched Capacitor Circuits**

Confirmation No. *To Be Assigned*

Art Unit: *To Be Assigned*

Examiner: *To Be Assigned*

Atty. Docket: 1875.1250002.

**Information Disclosure Statement**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

Listed on accompanying Form PTO-1449 are documents that may be considered material to the examination of this application, in compliance with the duty of disclosure requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98.

Where the publication date of a listed document does not provide a month of publication, the year of publication of the listed document is sufficiently earlier than the effective U.S. filing date and any foreign priority date so that the month of publication is not in issue. Applicants have listed publication dates on the attached PTO-1449 based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated.

Applicants reserve the right to establish the patentability of the claimed invention over any of the information provided herewith, and/or to prove that this information may not

be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered.

This statement should not be construed as a representation that a search has been made, or that information more material to the examination of the present patent application does not exist. The Examiner is specifically requested not to rely solely on the material submitted herewith.

Applicants have checked the appropriate boxes below.

- ☐ 1. Statement under 37 C.F.R. 1.704(d). Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application and this communication was not received by any individual designated in 37 C.F.R. § 1.56(c) more than thirty days prior to the filing of this information disclosure statement.
- ☒ 2. Filing under 37 C.F.R. § 1.97(b). This Information Disclosure Statement is being filed within three months of the date of filing of a national application other than a continued prosecution application (CPA), OR within three months of the date of entry of the national stage as set forth in 37 C.F.R. § 1.491 in an international application, OR before the mailing date of a first Office Action on the merits OR before the mailing of a first Office Action after the filing of a request for continued examination under 37 C.F.R. § 1.114. No statement or fee is required.
- ☐ 3. Filing under 37 C.F.R. § 1.97(c). This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection, or Notice of Allowance, or an action that otherwise closes prosecution in the application.
  - ☐ a. Statement under 37 C.F.R. § 1.97(e)(1). I hereby state that each item of information contained in this Information Disclosure Statement was

first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(1).

- ☐ b. Statement under 37 C.F.R. § 1.97(e)(2). I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(2).
- ☐ c. Attached is our PTO-2038 Credit Card Payment Form [OR] Check No. \_\_\_\_\_ in the amount of \$ \_\_\_\_\_ in payment of the fee under 37 C.F.R. § 1.17(p).
- ☐ 4. Filing under 37 C.F.R. § 1.97(d) This Information Disclosure Statement is being filed more than three months after the U.S. filing date and after the mailing date of a Final Rejection or Notice of Allowance, but before payment of the Issue Fee. Attached is our PTO-2038 Credit Card Payment Form [OR] Check No. \_\_\_\_\_ in the amount of \$ \_\_\_\_\_ in payment of the fee under 37 C.F.R. § 1.17(p); in addition:
  - ☐ a. Statement under 37 C.F.R. § 1.97(e)(1). I hereby state that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(1).
  - ☐ b. Statement under 37 C.F.R. § 1.97(e)(2). I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more

than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(2).

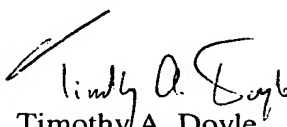
- ☒ 5. Documents AL1-AN1 were cited in a European search report by a foreign patent office in a counterpart foreign application. Submission of an English language version of the search report that indicates the degree of relevance found by the foreign office is provided as document AS2 in satisfaction of the requirement for a concise explanation of relevance. 1138 OG 37, 38.
- ☐ 6. A concise explanation of the relevance of the non-English language document(s) appears below:
- ☐ 7. Copies of the documents were cited by or submitted to the Office in an IDS that complies with 37 C.F.R. § 1.98(a)-(c) in Application No. \_\_\_\_\_, filed \_\_\_\_\_, which is relied upon for an earlier filing date under 35 U.S.C. § 120. Thus, copies of these documents are not attached. 37 C.F.R. § 1.98(d).

It is respectfully requested that the Examiner initial and return a copy of the enclosed PTO-1449, and indicate in the official file wrapper of this patent application that the documents have been considered.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

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Date: 23 Feb 04

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SKGF Rev. 12/1802 mac; Rev. 08/07/03 svb

FORM PTO-1449

## INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.  
1875.1250002APPLICATION NO.  
*To Be Assigned*APPLICANT  
Sandeep K. GUPTAFILING DATE  
February 20, 2004GROUP  
*To Be Assigned*

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA1	4,229,692	10/1980	Graeme			
	AB1	5,159,277	10/1992	Mount			
	AC1	5,243,235	09/1993	Wakayama <i>et al.</i>			
	AD1	5,572,154	11/1996	Rakers <i>et al.</i>			
	AE1	5,608,614	03/1997	Ohnishi <i>et al.</i>			
	AF1	5,698,999	12/1997	Etoh <i>et al.</i>			
	AG1	5,736,895	04/1998	Yu <i>et al.</i>			
	AH1	5,745,002	04/1998	Baschiroto <i>et al.</i>			
	AI1	5,753,815	05/1998	Murata			
	AJ1	5,838,175	11/1998	Hsieh			
	AK1	5,892,473	04/1999	Shin			

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL1	WO 94/01936	01/1994	PCT	H03K	19/0175	Yes No
	AM1	WO 00/79682 A1	12/2000	PCT	H03K	17/14	Yes No
	AN1	FR 2 793 970	11/2000	France	H03K	17/16	Yes No
	AO1						Yes No
	AP1						Yes No

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AR	1	<i>Low-voltage, High-Speed, High-Precision Switched-capacitor Circuits: Slide 5 of 25, from <a href="http://kabuki.eecs.berkeley.edu/~abo/slides/quals/quals.5.html">http://kabuki.eecs.berkeley.edu/~abo/slides/quals/quals.5.html</a>, 1 page (April 25, 1996).</i>
	AS	1	<i>Low-voltage, High-Speed, High-Precision Switched-capacitor Circuits: Slide 8 of 25, from <a href="http://kabuki.eecs.berkeley.edu/~abo/slides/quals/quals.8.html">http://kabuki.eecs.berkeley.edu/~abo/slides/quals/quals.8.html</a>, 1 page (April 25, 1996).</i>
	AT	1	<i>Low-voltage, High-Speed, High-Precision Switched-capacitor Circuits: Slide 11 of 25, from <a href="http://kabuki.eecs.berkeley.edu/~abo/slides/quals/quals.11.html">http://kabuki.eecs.berkeley.edu/~abo/slides/quals/quals.11.html</a>, 1 page (April 25, 1996).</i>

EXAMINER

DATE CONSIDERED

**EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA2	5,960,183	09/1999	Toumazou <i>et al.</i>			
	AB2	6,150,971	11/2000	Naylor <i>et al.</i>			
	AC2	6,198,296 B1	03/2001	Ivanov			
	AD2						
	AE2						
	AF2						
	AG2						
	AH2						
	AI2						
	AJ2						
	AK2						

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL2						Yes No
	AM2						Yes No
	AN2						Yes No
	AO2						Yes No
	AP2						Yes No

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AR	2	Norsworthy, S.R. <i>et al.</i> , <i>Delta-Sigma Data Converters</i> , IEEE Press, pp. v-xiv and 334-348 (1997).
	AS	2	Copy of European Search Report for Appln. No. EP 02 25 0157, issued July 23, 2003, 3 pages.
	AT	2	

EXAMINER

DATE CONSIDERED

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